



# TRANSMITTAL FORM

(to be used for all correspondence after initial filing)  
Total Number of Pages in This Submission **15**

Application Number **09/626,574**  
Filing Date **7/27/00**  
First Named Inventor **May**  
Group Art Unit **2816**  
Examiner Name **Luu, A**  
Attorney Docket Number **SIG000049**

- ☐ Fee Transmittal Form  
☐ Fee Attached  
☒ Amendment / Response  
☐ After Final  
☐ Affidavits/declaration(s)  
☒ Extension of Time Request  
☐ Express Abandonment Request  
☐ Information Disclosure Statement  
☐ Certified Copy of Priority Document(s)  
☐ Response to Missing Parts/ Incomplete Application  
☐ Response to Missing Parts under 37 CFR 1.52 or 1.53

## ENCLOSURES (check all that apply)

- ☐ Assignment Papers (for an Application)  
☐ Drawing(s)  
☐ Licensing-related Papers  
☐ Petition Routing Slip (PTO/SB/69) and Accompanying Petition  
☐ To Convert a Provisional Application  
☐ Power of Attorney, Revocation, Change of Correspondence Address  
☐ Terminal Disclaimer  
☐ Small Entity Statement  
☐ Request for Refund

- ☐ After Allowance Communication to Group  
☐ Appeal Communication to Board of Appeals and Interferences  
☐ Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)  
☐ Proprietary Information  
☐ Status Letter  
☐ Additional Enclosure(s) (please identify below):

Remarks

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

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Signature					
Individual Name	Timothy W. Markison	Reg. No.	33,534	Date	8/20/01

## CERTIFICATE OF MAILING

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PATENT APPLICATION  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicant:** May  
**Serial No:** 09/626,574  
**Filing Date:** 7/27/00  
**Title:** Edge Sensitive Detection Circuit

**Examiner:** Luu, A  
**Art Group:** 2816  
**Docket No:** SIG000049

8/29/01  
Smith

Date: 8/21/01

Honorable Commissioner of  
Patents and Trademarks,  
Washington, D.C. 20231

In response to an Office Action mailed on 5/3/01 regarding the above-captioned patent application, the applicant respectfully submits the following amendment and response.

**CLEAN VERSION OF REPLACEMENT PARAGRAPH/SECTION/CLAIM**

Please rewrite claims 2, 3, 10, 12, and 18 as follows:

2. (amended) The edge sensitive detection circuit of claim 1, wherein the soft latch module comprises:

a first inverting logic element; and

a second inverting logic element having an output impedance, wherein an input of the first inverting logic element is coupled to the output impedance, wherein an input of the second inverting logic element is coupled to an output of the first inverting logic element, and wherein the pulse signal is received at the coupling of the input

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